



# ELECTRONIC INFORMATION DISCLOSURE STATEMENT RECEIVED

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Title of Invention	METHOD AND APPARATUS FOR CONTROL OF RECEIVE DATA																																																																																
<p>Application Number: 09/475614</p> <p>Confirmation Number: 6580</p> <p>First Named Applicant: Gilbert Wolrich</p> <p>Attorney Docket Number: 10559-137001</p> <p>Art Unit: 2155</p> <p>Examiner: David Y. Eng</p> <p>Search string: ( 6079008 or 6085215 or 6085294 or 6092127 or 6092158 or 6112016 or 6134665 or 6141689 or 6141765 or 6144669 or 6145054 or 6157955 or 6160562 or 6182177 or 6195676 or 6199133 or 6201807 or 6212542 or 6212611 or 6216220 or 6223207 or 6223238 or 6223279 or 6247025 or 6256713 or 6272616 or 6275505 or 6279113 or 6298370 or 6307789 or 6324624 or 6345334 or 6347344 or 6356962 or 6360262 or 6389449 or 6393483 or 6415338 or 6427196 or 6430626 or 6434145 or 6463072 or 6532509 or 6552826 or 6560667 or 6577542 or 6584522 or 6587906 or 6606704 or 6625654 ).pn.</p>																																																																																	
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Examiner Name	Date
DAVID Y. FENG PRIMARY EXAMINER	1/05



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<p>Application Number: 09/475614</p> <p>Confirmation Number: 6580</p> <p>First Named Applicant: Gilbert Wolrich</p> <p>Attorney Docket Number: 10559-137001</p> <p>Art Unit: 2155</p> <p>Examiner: David Y. Eng</p> <p>Search string: ( 3478322 or 3792441 or 3940745 or 4130890 or 4400770 or 4514807 or 4523272 or 4745544 or 4866664 or 5140685 or 5142683 or 5155831 or 5155854 or 5168555 or 5173897 or 5263169 or 5347648 or 5367678 or 5390329 or 5392391 or 5392411 or 5392412 or 5404482 or 5404464 or 5432918 or 5448702 or 5450351 or 5452437 or 5459842 or 5463625 or 5467452 or 5517648 or 5542070 or 5542088 or 5544236 or 5550816 or 5557766 or 5568617 or 5574922 or 5592622 or 5613071 or 5613136 or 5623489 or 5627829 or 5644623 or 5649157 or 5680641 or 3373408 or 5630130 or 5659687 or 20040039895 or 20040054880 or 20040071152 or 20040073728 or 20040073778 or 20040098496 or 20040109369 ),pn.</p> <p><b>RECEIVED</b> OCT 20 2004 Technology Center 2100</p>							
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### US Published Applications

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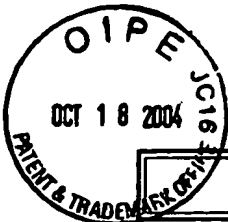
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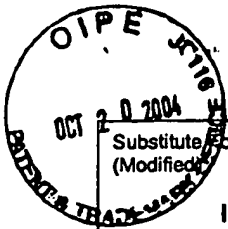
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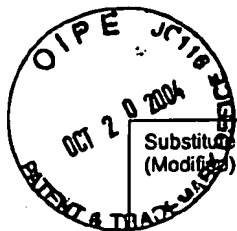
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U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
29	AA	09/387,109	08/31/1999	Adiletta et al.			
↓	AB	09/473,571	12/28/1999	Wolrich et al.			
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	AF						

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Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
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	AH	0 464 715	01/08/1992	Europe				
	AI	0 633 678	01/11/1995	Europe				
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	AW	WO 01/50247	07/12/2001	WIPO				
	AX							

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Sheet 2 of 2Substitute Form PTO-1449  
(Modified)U.S. Department of Commerce  
Patent and Trademark OfficeAttorney's Docket No.  
10559-137001Application No.  
09/475,614**Information Disclosure Statement  
by Applicant**

(Use several sheets if necessary)

(37 CFR §1.98(b))

Applicant  
Gilbert Wolrich et al.Filing Date  
December 30, 1999Group Art Unit  
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**Other Documents (include Author, Title, Date, and Place of Publication)**

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29	AY	Byrd et al., "Multithread Processor Architectures," <i>IEEE Spectrum</i> , Vol. 32, No. 8, New York, 1 August 1995, pp. 38-46.
	AZ	Doyle et al., <i>Microsoft Press Computer Dictionary</i> , 2 <sup>nd</sup> ed., Microsoft Press, Redmond, Washington, USA, 1994, p. 326.
	AAA	Fillo et al., "The M-Machine Multicomputer," <i>IEEE Proceedings of MICRO-28</i> , 1995, pp. 146-156.
	ABB	Gomez et al., "Efficient Multithreaded User-Space Transport for Network Computing: Design and Test of the TRAP Protocol," <i>Journal of Parallel and Distributed Computing</i> , Academic Press, Duluth, Minnesota, USA, vol. 40, no. 1, 10 January 1997, pp. 103-117.
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	AEE	Hyde, R., "Overview of Memory Management," <i>Byte</i> , vol. 13, no. 4, 1998, pp. 219-225.
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	AKK	Turner et al., "Design of a High Performance Active Router," Internet Document, <i>Online</i> , 18 March 1999.
	ALL	Vibhatavanijit et al., "Simultaneous Multithreading-Based Routers," <i>Proceedings of the 2000 International Conference of Parallel Processing</i> , Toronto, Ontario, Canada, 21-24 August 2000, pp. 362-359.
✓	AMM	Wazlowski et al., "PRSIM-II computer and architecture," <i>IEEE Proceedings, Workshop on FPGAs for Custom Computing Machines</i> , 1993.
	ANN	

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